

N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I _D (A) ^{a, e}	Q _g (Typ.)		
30	0.98 at V _{GS} = 10 V	190	59 nC		
30	1.4 at V _{GS} = 4.5 V	190			

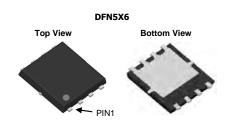
FEATURES

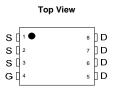
- **DT-Trench Power MOSFET**
- 100 % R_g and UIS Tested

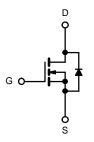


APPLICATIONS

- DC/DC Converter
- Synchronous Rectification







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	S (T _A = 25 °C, unle	ess otherwise not	ed)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	V
	T _C = 25 °C		190 ^{a, e}	
Continuous Drain Current (T _J = 175 °C)	T _C = 70 °C	I _D	150 ^e	
Continuous Diam Current (1) = 175 C)	T _A = 25 °C	'D	46 ^{b, c}	A
	T _A = 70 °C		39 ^{b, c}	^
Pulsed Drain Current		I _{DM}	760	
Avalanche Current Pulse		I _{AS}	186	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	1500	mJ
Continuous Source-Drain Diode Current	T _C = 25 °C	la la	180 ^{a, e}	А
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	33.5 ^{b, c}	^
Maximum Power Dissipation	T _C = 25 °C		300 ^a	
	T _C = 70 °C	P _D	210	w
	T _A = 25 °C	' D	5.92 ^{b, c}	VV
	T _A = 70 °C		4.15 ^{b, c}	
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	20	25	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.4	0.5		

Notes:

- a. Based on T_C = 25 °C. b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature.

Rev. 1.0



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	ι _D – 200 μΛ		- 7.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	190			Α	
Drain-Source On-State Resistance ^a	Ь	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.98	1.25		
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		1.4	1.8	mΩ	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 5 V, I _D = 20 A		142		S	
Dynamic ^b							
Input Capacitance	C _{iss}			3890		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		2220			
Reverse Transfer Capacitance	C _{rss}			157			
Total Gate Charge	Q_{g}			59		nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		9.9			
Gate-Drain Charge	Q_{gd}			9.6			
Gate Resistance	R _g	f = 1 MHz		5.5		Ω	
Turn-On Delay Time	t _{d(on)}			16			
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.555 Ω		12			
Turn-Off Delay Time	t _{d(off)}	$I_{D} \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		77			
Fall Time	t _f			11			
Turn-On Delay Time	t _{d(on)}			53		ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		180		- - -	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 20$ A, $V_{GEN}=4.5$ V, $R_g=1$ Ω		55			
Fall Time	t _f			12			
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			190	A	
Pulse Diode Forward Current ^a	I _{SM}				920		
Body Diode Voltage	V_{SD}	I _S = 1 A			1	V	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs, T _J = 25 °C		23		ns	
Body Diode Reverse Recovery Charge	Q _{rr}			80		nC	
Reverse Recovery Fall Time	t _a			28		20	
Reverse Recovery Rise Time	t _b			25		ns	

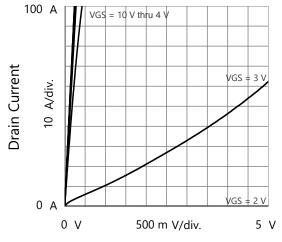
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

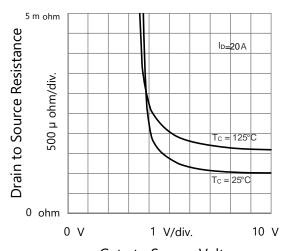
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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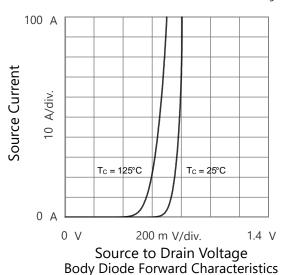
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Drain to Source Voltage Output Characteristics



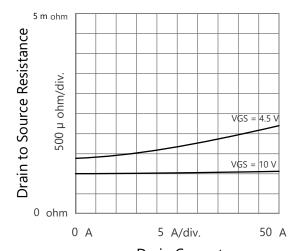
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



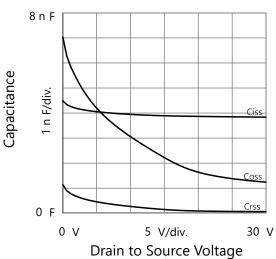
O A

O V 500 m V/div. 5 V

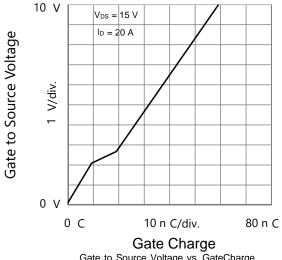
Gate to Source Voltage Transfer Characteristics



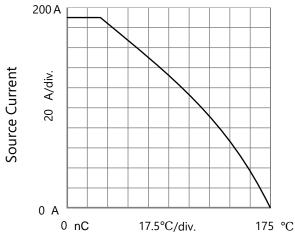
Drain Current
Drain to Source Resistance vs. Drain Current



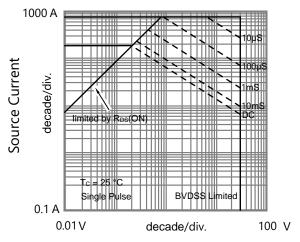
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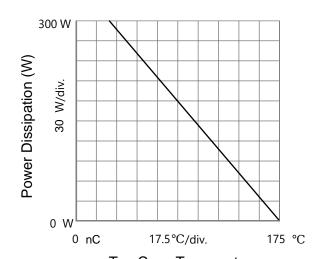




 $T_{C} \textbf{ - Case Temperature}_{\text{Current Derating}}$

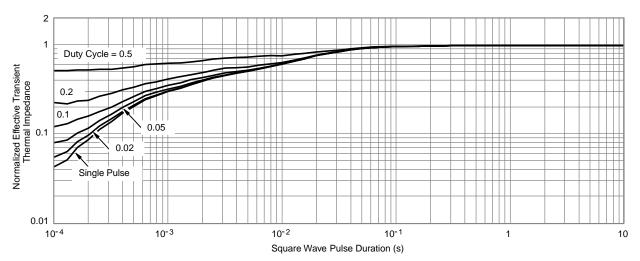


Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature

Power Derating



Normalized Thermal Transient Impedance, Junction-to-Case

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